



Mobile Computing Products *D*ivision

---

## Élan Device Evaluation Board Errata, Rev. 2.2

### **Errata # : 1**

Entry Date : 2/7/95

Status : OPEN

Description : Something to watch for on the board, the Floppy, Hard Drive, and CRT back power the board, this is especially evident in MicroPower Off mode. The Floppy and HDD can cause the Elan Core current to rise from 24uA up to 200uA where it will drift down slowly as the planes settle out around 0.2-0.5volts. The CRT will cause VCCSYS5 to sit up around 0.5 volts but this doesn't seem to affect the Elan core current.

Solution :

Schematic pages affected :

ELAN revisions affected by this issue :

ELAN Revision : A,B

Rework & Design Example Schematic Revisions that will address this issue :

Rework :

DES :

Comments :

3/21/95 - Same as 2.1 Errata #20

---

---

**Errata # : 2**

Entry Date : 2/28/95

Status : OPEN

Description : Local Bus cannot operate at speeds greater than 9.2 Mhz due to the large capacitive loading on the Local Bus signals.

Solution : Under Investigation

Schematic pages affected :

ELAN revisions affected by this issue :

ELAN Revision : A, B

Rework & Design Example Schematic Revisions that will address this issue :

Rework :

DES :

Comments :

3/21/95 : Same as 2.1 erratta #21

---

**Errata # : 3**

Entry Date : 4/17/95

Status : OPEN

Description : If the Evaluation board is configured to support the ability to keep DRAM powered up while in uPower mode, on initial power ON (bench supply), ELAN will come up in an undefined state which will draw excessive current from the power source. The implementation on the Evaluation board is not the recommended method for implementing this feature. The VMEM plane should not come up on initial power ON.

Solution :

Schematic pages affected :

ELAN revisions affected by this issue :

ELAN Revision : B

Rework & Design Example Schematic Revisions that will address this issue :

Rework : N/A

DES : N/A

Comments :

---

**Errata # : 4**

Entry Date : 4/17/95

Status : OPEN

Description : uPower mode will not work in Local bus mode due to the method that we use to control the power switching on the Evaluation board.

Solution :

Schematic pages affected :

ELAN revisions affected by this issue :

ELAN Revision : B

Rework & Design Example Schematic Revisions that will address this issue :

Rework :

DES :

Comments :

4/17/95 : This issue is not a very big deal on the Evaluation board since the Local bus on the Evaluation board will not work at speeds greater than 9.2 Mhz anyway.

---

**Errata # : 5**

Date : 5/22/95

Description : The VCC5 2 pin header, pin 1 on the 2.2 board is connected to the VCCSYS power plane. This will result in incorrect power readings on the VCC5 & possibly the VCCSYS planes.

Solution : Implement 2.2 rework rev A. The VCC5 header is removed from its current location & placed between pin 2 of its original position & the positive side of C3.

Schematic pages affected : N/A. (CAD issue)

ELAN revisions affected by this issue :

ELAN Revision : A, B

Rework & Design Example Schematic Revisions that will address this issue :

Rework : N/A

DES : N/A

Comments :

---

**Errata # :**

Date :

Description :

Solution :

Schematic pages affected :

ELAN revisions affected by this issue :

ELAN Revision :

Rework & Design Example Schematic Revisions that will address this issue :

Rework :

DES :

Comments :

---

**Errata # :**

Date :

Description :

Solution :

Schematic pages affected :

ELAN revisions affected by this issue :

ELAN Revision :

Rework & Design Example Schematic Revisions that will address this issue :

Rework :

DES :

Comments :